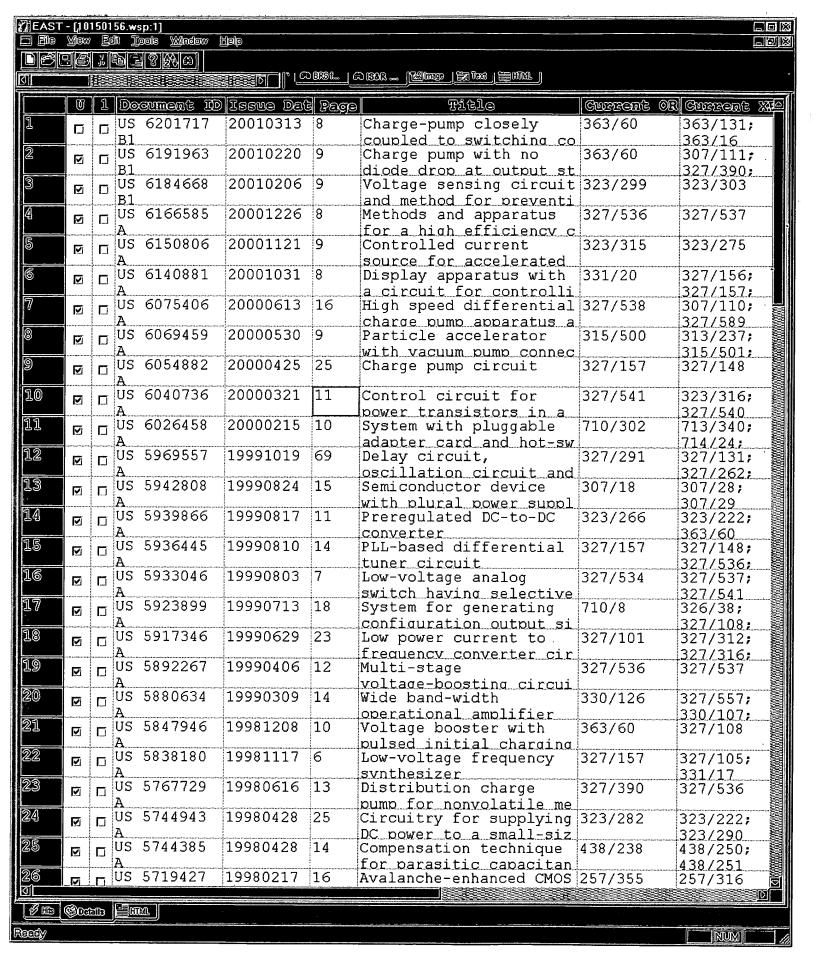
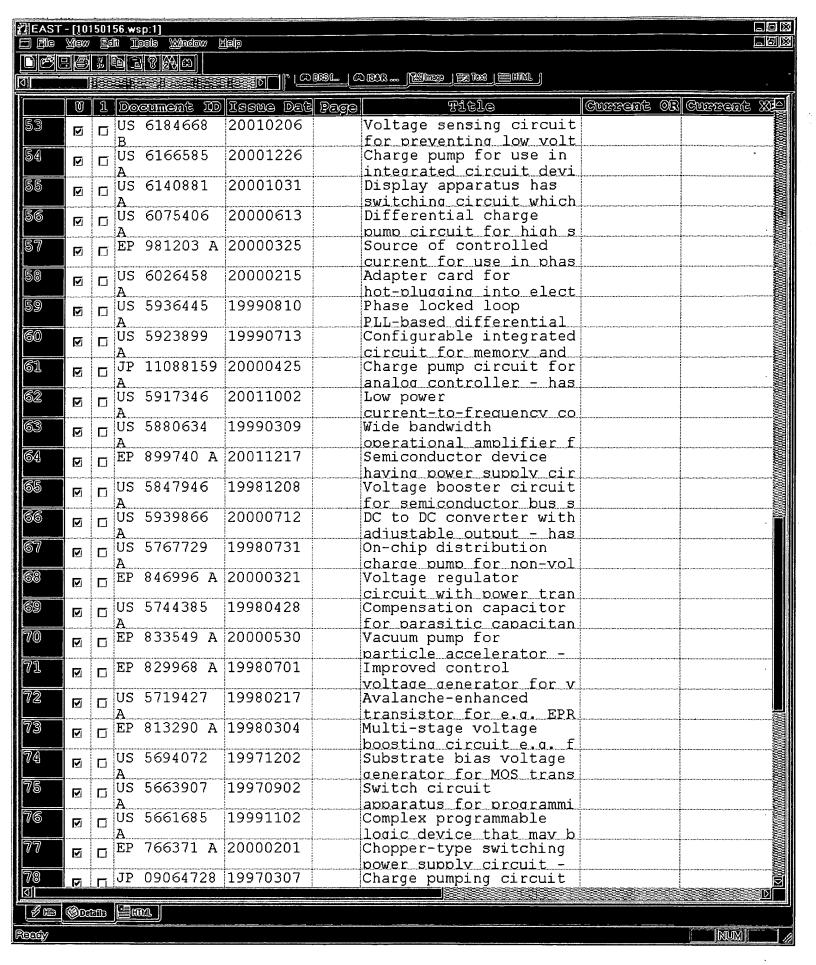
4	Туре	L #	Hits	Search Text	DBs	Time Stamp	Comment s
14	BRS	L14		clock adj generating same delay	:	2002/10/1 7 10:32	
15	BRS	L15		14 and ((DLL delay adj locked adj loop) (PLL phase adj locked adj loop))	USPA T; US-P GPUB ; JPO; DERW ENT	2002/10/1 7 10:34	



				ools <u>W</u> indow <u>I</u>	<u>-t</u> elp					BIX	
		% [d e	8 2							
1						BRS f	IS&R Mage 資 Text 量 HTML			0	
<u> </u>											
	U		<u> </u>	cument ID		Page	Title	Current OR		閆	
27	Ø		US	5694072	19971202		Programmable substrate	327/537	327/536;		
	.,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,		Α				bias generator with cur		327/540	_3	
28	v		US	5663907	19970902		Switch driver circuit	365/185.18	365/185.2	73	
		.,	Α		10070000		for providing small sec	221/0	202 (4.52		
.29	V		US	5663686	19970902		Charge pump circuit and	331/8	327/157;		
50		.,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	A	F C C 1 C O F	1007000		phase locked loop circu	365/185.22	331/17;		
30	Ø		US	5661685	19970826		Programmable logic	303/183.22	365/185.0	1	
31			A	E642212	10070624		device with configurabl	365/105 25	326/00.		
21	V	П	7 0.5	5642313	19970024			303/103.23			
32			HS.	5627488	19970506			327/261			
J2	Ø		7	3027400	13370300		· · · · · · · · · · · · · · · · · · ·	521/201	1		
33			LIS	5623222	19970422			327/259			
	Ø		A	0020222	13313122	:		,		fill	
34	V		US	5604694	19970218			365/96			
		L	A				J 1 1	•			
35	Ø		ÛS	5532916	19960702		Voltage converting	363/62	307/110		
	IX1	IJ	A				circuit and multiphase				
36	V	П	US	5495436	19960227		Anti-fuse ROM	365/96	327/536;		
	13.1		A	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,		i !	programming circuit		365/225.7		
37	Ø		US	5465061	19951107		Low-consumption	327/112	327/108;		
		1	Α				low-noise charge-pump c		327/111;		
38	v		US	5461557	19951024			363/60	307/110		
			Α		10050500			0.25 (1.00 0.1	A		
39	V		US	5412603	19950502			365/189.01			
			A	F4040F3	10050404		for programming floatin	227/100			
40	Ø		U.S	5404053	19950404			327/108			
41			A	5302205	10050221			363/50			
1 T	Ø		7	0002200	13330221			303/33			
42		_	IIS	5381056	19950110			326/21			
	v		A	0001000	13300110		:	320,21			
43	Ø	П	ŪS	5373257	19941213			331/8			
		L	Ā				;	, -			
44	v	П	US	5359298	19941025		VCO having	331/2	331/108B;		
1	(2)	ا لــه	Α				voltage-to-current conv		331/14;		
45	Ø	П	US	5302919	19940412		VCO having	331/2	331/108B;		
			Α						331/14;		
46	v		US	5234319	19930810		Sump pump drive system	417/40	417/45		
1.5			A	E0101E6	10000510	ļ		222	000 (60)		
47	v		US	5212456	19930518			330/261			
48			A	E10000	10020110	·		227/50			
40	Ø		_	5180929	1990119			327/50			
49			A HS	1917510	10900711			327/536			
4.9	V		γ.	404/313	19090711			· ·			
50		_	A US	4354124	19821012			327/12			
	Ø		A	1501121	13021012			321712			
51	_	_	ĔΡ	1081836	20020510				J.D/J20		
	Ø		A.								
52	D.	П	US	6191963	20010220		Integrated circuit with			- All	
1	151										
乡 Hits	⊘ De1	ails	₽₽Н	TML							
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ij						BRS f	NS&R 図 Image 図 Text 圖 HTML			
	U	1	Do	cument ID	Issue Dat	Page	Title	Current OR	Current	XE
74	V		US	5694072	19971202		Substrate bias voltage			
75		 	A	5663907	19970902		generator for MOS trans Switch circuit			
13			D'S	3663307	19970902		apparatus for programmi			
76	v	П	US	5661685	19991102		Complex programmable			
		ļ	Α			***************************************	logic device that may b			
77	V		EP	766371 A	20000201		Chopper-type switching			
78		<u> </u>	JP	09064728	19970307		<pre>power supply circuit - Charge pumping circuit</pre>			
10	V		A	03004720	12270307		in PLL - in which up an			CANADO
79	v	П	FR	2738424	19990803		MOS transistor low			
		ļ	Α				voltage analog switch -			
80	V		US	5604694	19970218		Charge pump circuit for		7	2000
81		<u></u>	A .TP	08190798	20010611	***************************************	effectively providing p Non-volatile			No.
-	Ø		A	00150150	20010011		semiconductor memory e.			WWW.
32	v	П	EΡ	713165 A	19960522		Voltage boost circuit			2000
							for flash EPROM memorv		ļ	
33	⊡	П	ì	08070242	19990715		Delay circuit used for		ranco de la companya del companya de la companya de la companya del companya de la companya de l	2,000
3 4	1		A .TP	08065111	19990715		PMOS IC - has charging Oscillation circuit for		•	
•	V		A	00000111	13330110		semiconductor integrate			********
35	Ø	П	US	5495436	19960227		On-chip programming			TWO COLUMN
			A_				circuit for anti-fuse e			er er er
36	M		EΡ	681296 B	20011025		Programming method for			W.W.
37	1	<u> </u>	EР	614282 A	19980102		floating gate memory ce Charge pump circuit for			
	Ø	П					frequency synthesiser -		The state of the s	W.C.C.
88	V		ΕP	595632 A	19980212		Phase synchronisation	and the state of t		2002202
39		ļ	תים	587931 A	20000115		circuit for magnetic di			
) 9	Ø		СP	30/931 A	20000115		Complementary MOSFET buffer circuit - uses t		The state of the s	in the second
90	Ø		ΕP	585925 A	19980813		Charge-pump step-up			
	157	1		***************************************			type voltage converting	781.arti.arti.arti.arti.arti.arti.arti.arti		, and a second
91	V		EΡ	574646 A	19980416		Circuit controlling			92949
)2			IIC	5234319	19930810		maximum current in MOS			
	V		A	3234313	19930010		Building sump pump drive system with norma			WWW.
93	Ø	П	US	5212456	19930518		Linear voltage			
			Α				amplifier circuit - has	***************************************		
4	V		EP	540948 A	19930512		Regulated charge pump			
95			IIS	5180929	20010307		for voltage generator f Voltage measuring			
			A	0100929	20010507		circuit for unknown vol			
96	Ø	Г	US	5302919	19961120		Voltage-controlled			
			A_	1017530			oscillating circuit for			
7	V	□	US	4847519	19890711		Integrated zero hold			
8	_		A DE	3022746	19810108		current and delay compe Injection logic digital			
			A	0022140	25010100		phase comparator - simu			
							The state of the s			F
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